

January 28, 1964

Digital Readout ComparATOR

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The first unit was shipped to [REDACTED] on Friday January 17. Its arrival was confirmed on Monday by telephone to [REDACTED]. The only operation which [REDACTED] was not able to check was the "acknowledge error receive". He used a bit by bit simulation of the error word, but he was not able to simulate the dataphone of the error word because they have no dataphone.

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The second unit is built and being checked. [REDACTED] says the checkout is going much faster on the second one and he should be completed with it by the end of the week. There will be a delay in completing it however because switches went bad. During checkout of the front panel, 2 of the 5 readout character switches developed an open circuit in the Normally Open position. [REDACTED] has ordered replacements from [REDACTED] but they won't get in until Feb. 5. In addition, the mounting tabs on the alarm reset switch were broken. A replacement was also ordered from [REDACTED] which will be in at the same time as the other switches.

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An instruction manual was shipped with the first unit and a copy will also go with the second unit. A third copy of the instruction manual is available. If you would like to have that copy, [REDACTED] just ask [REDACTED] for it. He will be glad to send it to you. The manual has a complete set of schematics.

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When the counter sends a message to the computer, it waits for an answer. If an answer does not come in 3 seconds, an alarm rings. The waiting time is governed by an RC delay circuit, I meant to say decay circuit. The time can be changed by changing the capacitor. For 3 seconds they are using a 10 MF 25 V e-a capacitor. It is on board 2827-515 and is shown on schematic 2827-115. As you know, John, the time is directly proportional to the capacitance, so you can change it easily if you wish.

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